

NAPATECH ANNOUNCES NEW FEATURE SET FOR THEIR NT SERIES 1Gb AND 10Gb HIGH PERFORMANCE NETWORK ADAPTER CARDS

Product Displays at INTEROP in Las Vegas, Nevada

ANDOVER, Massachusetts, - April 29, 2008 – Napatech today announces the latest software release called TUSCON for their NT series of 1Gb and 10Gb FPGA based high performance network adapter cards. The TUSCON software release included the following new additional features:

- 2 or 5 tuple hash key generation
- Extended descriptor
- Support for up to 32 host buffers
- Multi CPU host buffer splitting
- New tools and application code examples

“The ability to generate a 2 or 5 tuple hash key in hardware can be used to recognize various IP/TCP/UDP flows and Netflow analysis. This means the host CPU saves instructions and more memory bandwidth is available for other processing. Hash key generation along with the increase in host buffers from 2 to 32 allows for a unique multi CPU host buffer splitting function that enables the adapter to distribute the processing of captured frames among the host CPUs. The algorithm used by the adapter for placing a captured frame in a host buffer is based on packet flow information. The advantages of intelligent distribution of captured frames on multiple buffers will increase the cache hit rate of the host CPU cores providing a higher user application performance.” says [Jens Christophersen](#), CTO at Napatech.

The new features described above are added to the already feature rich product line offered to Napatech’s OEM customers. The high performance adapters already include features such as line rate processing of packets up to 20Gb speeds, 10ns time-stamping of every packet, channel merging, 64 user programmable filters, packet classification, packet slicing and more.

The feature set supports Linux, FreeBSD, and Windows drivers, a programming interface and development tools.

About Napatech

Napatech is a leading OEM supplier of multi port 1Gb and multi port 10Gb high performance network adapter cards. The core idea is to off-load real-time/streaming protocols, payload analysis and control applications traditionally implemented in software or proprietary hardware. Napatech expects a huge growth in the demand for intelligent and programmable adapters as Ethernet speeds increase. Current PC architecture limits the amount of bandwidth that can be handled by the PCI bus and CPU/memory. Napatech has sales, marketing and R&D offices in Mountain View, California, Andover, Massachusetts and Copenhagen, Denmark.

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